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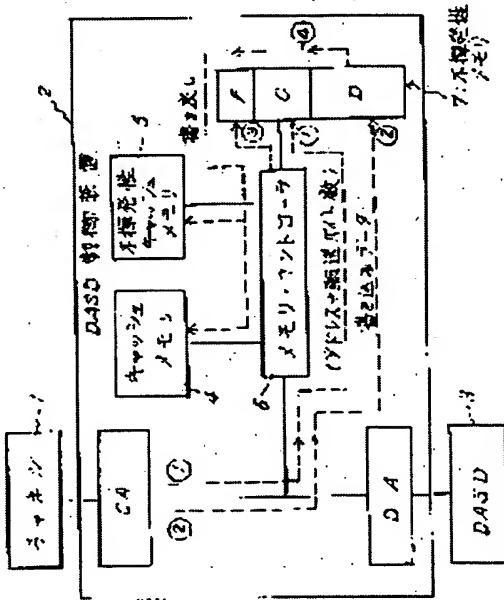
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## (54) BACKUP CONTROL SYSTEM OF FILE HIGH-SPEED WRITING MECHANISM

### (57) Abstract:

**PURPOSE:** To minimize the loss of data by holding data before process interruption even if a process is interrupted owing to a power failure, etc., during a writing process by the DASD high-speed writing mechanism.

**CONSTITUTION:** A DASD controller 2 is provided with a cache memory 4, a nonvolatile cache memory 5, a memory controller 6, and a nonvolatile memory 7 and when data are written, the write data are inputted to the nonvolatile memory 7 temporarily and written back to the cache memory 4 and nonvolatile cache memory 5. In this case, an address plus the number of transfer bytes are written in a control part C, and the write data are stored in a data area D; and the transfer completion flag of a flag part F is set and the data are written back to the cache memories 4 and 5 successively. Then a transfer completion flag is reset.



### LEGAL STATUS

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